

EMR-based Modelling and Control of LC-filtered Inverter

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MODELLING AND CONTROL OF A LC-FILTERED INVERTER

- System representation using EMR [Bouscayrol 2012] -

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- LC-filtered inverter -

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- Nonlinear load -

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LC-filtered inverter					<	Nonlinear load
Inverter	abc - dq ≪> ≪	ac LC filter €>	abc - <	• dq >	Diodes	dc LC filter > ≪>
Battery i_{inv} i_{Labc} $i_{i}\theta$ i_{Ldq} i_{Ldq} u_{Cdq} u_{Cdq} u_{Cdq} u_{Cdq} u_{Cdq} u_{Cdq} u_{Cdq} u_{Cdq} u_{Cdp} u_{Cabc} u_{D} u_{d} u_{d} u_{c}						
 Diodes principle 	On-state diodes	u _{dc}	i _{0a}	i _{0b}	i _{0c}	↓ dc LC filter
If $i_{dc} > 0$ or $u_{dc} > u_c$,	D1, D6	$u_{Ca} - u_{Cb}$	i _{dc}	$-i_{dc}$	0	$i_{dc} = \frac{1}{1}(u_D - u_c)$
then $u_{\rm D} = u_{dc}$	D1, D2	$u_{Ca} - u_{Cc}$	i _{dc}	0	$-i_{dc}$	$L_n s$
	D3, D2	$u_{Cb} - u_{Cc}$	0	i _{dc}	$-i_{dc}$	
else $u_D = u_c$.	D3, D4	$u_{cb} - u_{Ca}$	$-i_{dc}$	i _{dc}	0	$u_c = \frac{1}{C_n s} (i_{dc} - i_R)$
i i i i i i i i i i i i i i i i i i i	D5, D4	$u_{CC} - u_{Ca}$	$-i_{dc}$	0	i _{dc}	
	D5, D6	$u_{CC} - u_{Cb}$	0	$-i_{dc}$	i _{dc}	

- Inversion-based control -

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Tunning path:



- Inversion-based control -

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DSP: Digital signal processor



NOVEL SLIDING MODE CURRENT CONTROLLER



Without overshoot







SIMULATIONS AND RESULTS





The response time equal to the desired





CONCLUSIONS AND PERSPECTIVES



- Conclusions
 - Proposal and validation of a new method of decoupled sliding mode current control
 - The approach is based on average converter model instead of conventional switching model
 - ✓ Simulation results demonstrate the superiority over the traditional PI controller
- Future works
 - To apply this method to other grid-connected power electronics converters
 - To work on the grid-forming inverter control



BIOGRAPHIES AND REFERENCES

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Time for questions